

Amendments To the Claims:

Please amend the claims as shown.

1. (currently amended) A Method for regulating a jitter buffer (JP) for buffering a data packet stream ~~wherein comprising:~~

registering a transmission delay (d_p) due to buffering ~~is in each case registered for the~~
data packets ($DP1, DP2, DP3$) of the data packet stream;

continuously deriving weighted mean delay values (d_t) ~~are continuously derived from~~
registered transmission delays (d_p), ~~with wherein~~ a shorter transmission delay ~~being is~~ weighted
higher than a longer transmission delay; and

regulating a read-out speed (CLK) of the jitter buffer (JP) ~~is regulated~~ as a function of the
continuously derived weighted mean delay values (d_t) ~~in such a way so~~ that said values are
adjusted as a regulating variable to a predefined desired delay (sd_t).

2. (currently amended) A Method according to Claim 1, ~~characterized in that~~
wherein a new weighted mean delay value (d_t) is derived from a previously derived weighted
mean delay value and a currently registered transmission delay (d_p).

3. (currently amended) A Method according to ~~one of the preceding~~ Claims 1,
~~characterized in that wherein~~ a currently registered transmission delay (d_p) is
compared with a previously derived weighted mean delay value, and the weighting of the
currently registered transmission delay (d_p) is determined as a function of the result of the
comparison.

4. (currently amended) A Method according to Claim 3, ~~characterized in that~~ wherein the currently registered transmission delay (d_p) is weighted with a first predefined weight value (β_1) if the currently registered transmission delay (d_p) is shorter than the previously derived weighted mean delay value and is weighted with a second predefined weight value (β_2) if the currently registered transmission delay (d_p) is longer than the previously derived weighted mean delay value, with the first weight value (β_1) being larger than the second weight value (β_2).

5. (currently amended) A Method according to ~~one of the preceding~~ Claims 1, ~~characterized in that~~ wherein the regulating variable (d_t) is regulated by a single regulating circuit.

6. (currently amended) A Jitter buffer regulating circuit for regulating a jitter buffer (JP) for buffering a data packet stream with comprising:

a registration device (~~EE~~) for registering a transmission delay (d_p) due to buffering of a respective data packet (~~DP1, DP2, DP3~~) of the data packet stream;

a mean-forming device (~~ME~~) for continuously deriving weighted mean delay values (d_t) from registered transmission delays (d_p), with higher weighting of a shorter transmission delay compared to a higher transmission delay; and

a regulating device (~~RE~~) for adjusting the continuously derived weighted mean delay values (d_t) to a predefined desired delay (sd_t) by ~~means of~~ regulating a read-out speed (CLK) of the jitter buffer (JP) as a function of the continuously derived weighted mean delay values (d_t).

7. (new) A method according to Claim 2, wherein a currently registered transmission delay is compared with a previously derived weighted mean delay value, and the weighting of the currently registered transmission delay is determined as a function of the result of the comparison.

8. (new) A method according to Claim 2, wherein the regulating variable is regulated by a single regulating circuit.

9. (new) A method according to Claim 3, wherein the regulating variable is regulated by a single regulating circuit.

10. (new) A method according to Claim 4, wherein the regulating variable is regulated by a single regulating circuit.